

MAR ATHANASIOUS COLLEGE OF ENGINEERING

Government Aided, Autonomous Institution

Kothamangalam, Kerala, 686666.

Ph: +91 485 2822363, E-mail: office@mace.ac.in Web: www.mace.ac.in

Semester Grade Card Report S1 (R,S) M.Tech Exam, December 2025 (2024 Scheme) (SCHEME : 2024)

Branch : Computer Aided Structural Engineering

- A M24CE1T101 - Numerical Methods in Structural Engineering B M24CE1T102 - Theory of Elasticity and Plasticity C M24CE2T103 - Advanced Analysis of Structures
- J M24CE1R106 - Research Methodology and IPR D M24CE2E104D - Advanced Concrete Technology E M24CE1E105A - Structural Dynamics
- G M24CE1L107 - Advanced Structural Engineering Lab D M24CE1E104A - Prestressed Concrete E M24CE1E105C - Advanced Design of Steel Structures

Register Number	M24CE1T101	M24CE1T102	M24CE2T103	M24CE1R106	M24CE2E104D	M24CE1E105A	M24CE1L107	M24CE1E104A	M24CE1E105C
MAC25CECS01	C	D	B	B+	-	-	A	B	C
MAC25CECS02	B	C+	B	A	-	-	A	B	B

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Semester Grade Card Report S1 (R,S) M.Tech Exam, December 2025 (2024 Scheme) (SCHEME : 2024)

Branch : Computer Science and Engineering

A M24CS1T101 - Computational Intelligence

B M24CS1T102 - Advanced Database Management

C M24CS1T103 - Advanced Machine Learning

D M24CS1E104C - Advanced Data Mining

E M24CS1E105D - Advanced Computer Networks

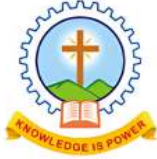
J M24CS1R106 - Research Methodology and IPR

G M24CS1L107 - Advanced Machine Learning Lab

Register Number	M24CS1T101	M24CS1T102	M24CS1T103	M24CS1E104C	M24CS1E105D	M24CS1R106	M24CS1L107
MAC25CSCE01	B	C+	C+	D	C+	C+	B
MAC25CSCE02	B+	A	B	C+	B+	B	B+
MAC25CSCE03	A+	S	S	A	A+	S	S
MAC25CSCE04	C+	B	A	B	B+	B+	B+
MAC25CSCE05	C+	C+	C+	C	B	B	B+
MAC25CSCE06	A	A	B+	B	A+	A+	A
MAC25CSCE07	A	B+	B+	B+	B+	B+	A
MAC25CSCE08	C	B+	B	C	B+	C+	B
MAC25CSCE09	B	B+	A	C+	B+	B+	B

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Branch : Power Electronics

A M24EE1T101 - Linear Algebra and Linear Systems

B M24EE1T102 - Analysis of Power Electronic Circuits

C M24EE1T103 - Switched Mode Power Converters

D M24EE1E104C - Soft Computing Techniques for PE Applications

E M24EE1E105B - Embedded Controllers for Power Converters

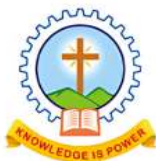
J M24EE1R106 - Research Methodology and IPR

G M24EE1L107 - Advanced Power Electronics Laboratory Lab

Register Number	M24EE1T101	M24EE1T102	M24EE1T103	M24EE1E104C	M24EE1E105B	M24EE1R106	M24EE1L107
MAC25EEPE01	B+	C+	B+	B	B+	B	S

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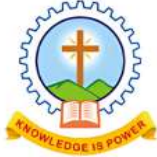
Branch : Structural Engineering and Construction Management

- A M24CE1T101 - Numerical Methods in Structural Engineering B M24CE1T102 - Theory of Elasticity and Plasticity C M24CE1T103 - Construction Planning, Scheduling and Control
- D M24CE1E104A - Prestressed Concrete D M24CE1E104B - Modern Construction Materials E M24CE1E105A - Structural Dynamics
- E M24CE1E105D - Construction Management and Engineering Economics J M24CE1R106 - Research Methodology and IPR G M24CE1L107 - Advanced Structural Engineering Lab
- E M24CE1E105C - Advanced Design of Steel Structures

Register Number	M24CE1T101	M24CE1T102	M24CE1T103	M24CE1E104A	M24CE1E104B	M24CE1E105A	M24CE1E105D	M24CE1R106	M24CE1L107	M24CE1E105C
MAC25CESC01	C	F	C+	B	-	D	-	B	A	-
MAC25CESC02	S	S	C	B	-	-	-	A+	A+	B+
MAC25CESC03	D	D	C	-	D	-	-	C+	B	P
MAC25CESC04	B	A+	B+	-	A	-	-	S	B+	A
MAC25CESC05	P	P	D	-	F	-	-	C+	B+	D
MAC25CESC06	B+	B	B	-	B+	B+	-	A	A	-
MAC25CESC07	A	B+	C+	-	C+	A+	-	B	A	-
MAC25CESC08	C	A	C+	-	C+	-	-	B+	B+	B
MAC25CESC09	B	C+	C	-	B+	-	-	A+	A	B
MAC25CESC10	D	D	C	P	-	D	-	D	C+	-
MAC25CESC11	A	B	C	-	C+	B	-	B	A+	-
MAC25CESC12	C+	B	B	-	B	-	-	B	A	C+
MAC25CESC13	B	B+	B	B	-	C	-	B+	B+	-
MAC25CESC14	C	B	C+	D	-	-	-	B+	B+	C+
MAC25CESC15	S	A+	C+	-	B	B+	-	A	A	-

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Branch : Thermal Power Engineering

A M24ME2T101 - Numerical Methods
For Engineers

B M24ME2T102 - Advanced Heat
Transfer

C M24ME2T103 - Advanced Fluid
Mechanics

D M24ME2E104B - Measurement
Methods in Thermal Engineering

E M24ME2E105B - Hydrogen and Fuel
Cell Technologies

J M24ME2R106 - Research
Methodology and IPR

G M24ME2L107 - Thermal Power
Systems Lab

Register Number	M24ME2T101	M24ME2T102	M24ME2T103	M24ME2E104B	M24ME2E105B	M24ME2R106	M24ME2L107
MAC25METP01	B	D	D	S	A	B+	S

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Branch : VLSI and Embedded Systems

- A M24EC1T101 - Digital CMOS VLSI Design B M24EC1T102 - FPGA - Based System Design C M24EC1T103 - Design with Advanced Microcontroller
- D M24EC1E104C - DSP Algorithms and Architecture E M24EC1E105A - Advanced Digital System Design J M24EC1R106 - Research Methodology and IPR
- G M24EC1L107 - HDL Lab

Register Number	M24EC1T101	M24EC1T102	M24EC1T103	M24EC1E104C	M24EC1E105A	M24EC1R106	M24EC1L107
MAC25ECVE01	F	F	P	C+	F	C	B
MAC25ECVE02	C+	C	B	B+	B	B+	B
MAC25ECVE03	B+	A	B+	A+	B+	C	A+
MAC25ECVE04	C	D	C+	B+	C+	C+	B
MAC25ECVE05	C+	D	C	A+	C	C	A+
MAC25ECVE06	B	C+	A	S	A	B+	A
MAC25ECVE07	C+	C+	B	A+	B	C+	B+
MAC25ECVE08	C+	C	B+	A	B	B+	B+
MAC25ECVE09	Ab	Ab	Ab	Ab	Ab	Ab	B
MAC25ECVE10	B	C+	A	A	B	B+	B
MAC25ECVE11	D	C	C	A	C	C+	B
MAC25ECVE12	C	D	C	B	C	B+	B
MAC25ECVE13	C+	B	B+	B	C+	C+	B

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